

CHALMERS

EXAMINATION / TENTAMEN

Course code/kurskod		Course name/kursnamn		
EDA-387		COMPUTER NETWORKS		
Anonymous code Anonym kod		Examination date Tentamensdatum	Number of pages Antal blad	Grade Betyg
EDA-387-7		27/8/2015	13	4

Solved task Behandlade uppgifter		Points per task Poäng på uppgiften	Observe: Areas with bold contours are to be completed by the teacher. Anmärkning: Rutor inom bred kontur ifylles av lärare.
No/nr			
1	1	6	
2	2	5	
3	3	7	
4	4	5	
5	5	2	
6	6	7	
7	7	5	
8	8	2	
9	9	1/2	
10	10	2.5	
11			
12			
13			
14			
15			
16			
17			
18			
Total examination poängs Summa poäng på tentamen		42	37

3
1 a) - User wanted to ask about the authoritative server
(ns1.ntnu.no) about the ~~main~~ hostname of the mail exchanger
that serves the 'ntnu.no' domain.

b) ~~to~~ First part of the question ntnu.no is the domain name
<the user is querying>
MX (is Mail Exchanger) the type of resource record we seek to find.

c) hostname: ns1.ntnu.no
IP-address: 129.241.0.208

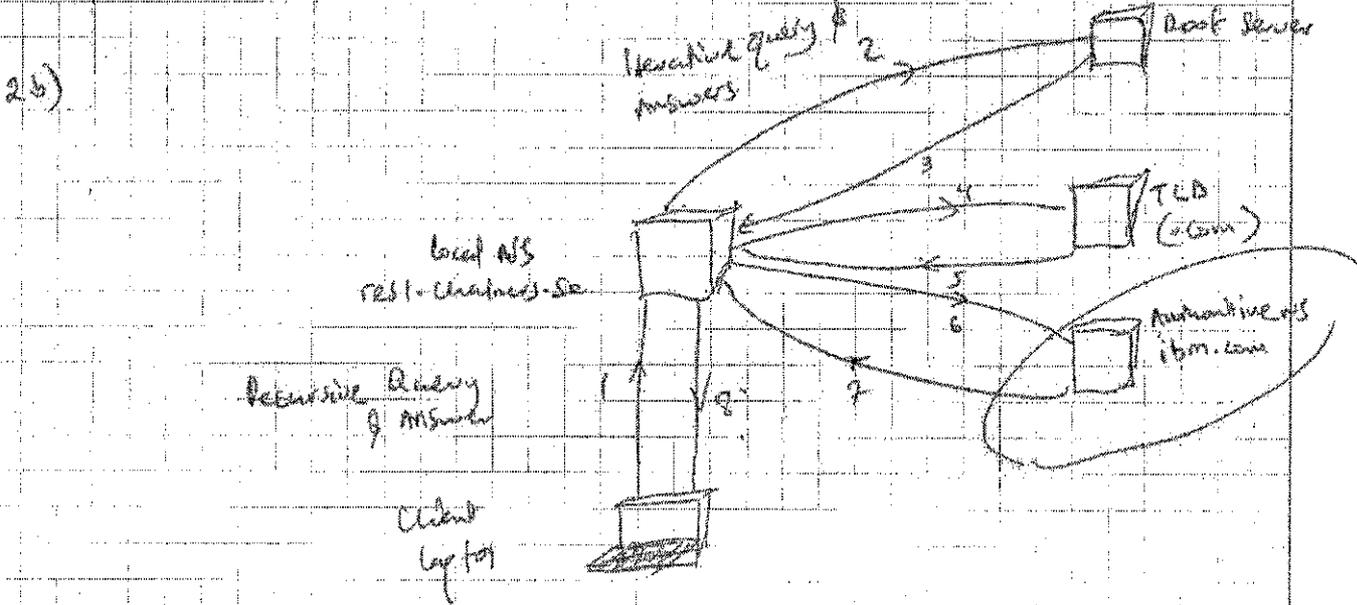
Yes it is an authoritative server because it is listed in the
authority section. Also the flag has aa implying it is
an authoritative answer.

d) Yes there is an answer in the reply and status is NOERROR.

The reply gives the ~~ns~~ hostname (ns1.ntnu.no) of the mail exchanger
responsible for the ntnu.no domain with a preference value of 10.
The type of answer is MX (mail exchanger)

The total time to live: TTL = 60 seconds the time for which
the answer is valid before it can be refreshed.

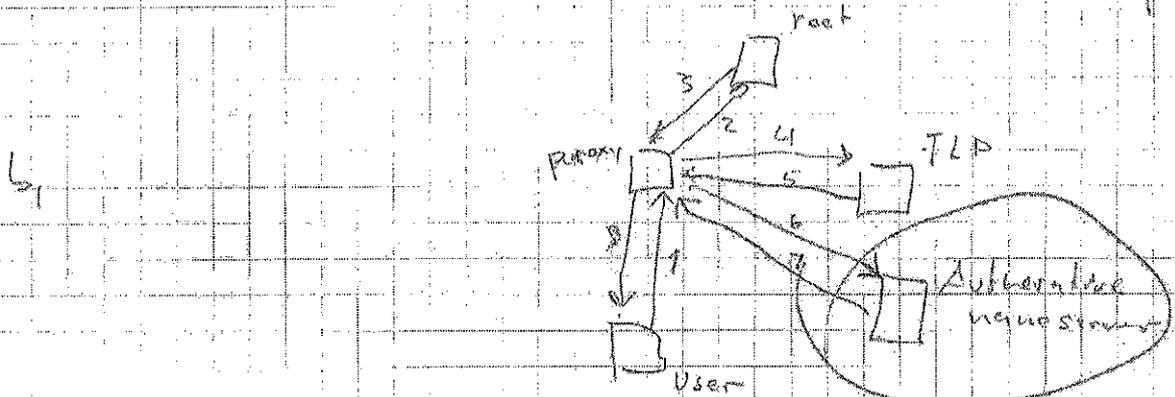
2a) The client has queried to get information about hostname of the name server that can give information about the domain that hosts ibm.com



1. Client makes recursive query to local NS (192.168.1.254)
2. Since local NS does not have the answer, it makes recursive queries as follows
 2. local NS contacts root name server about top level domain (TLD) Server of .com
 3. Root NS answers with the TLD (.com) domain server
 4. local NS queries .com about server ibm.com
 5. .com server responds with answers for the ibm.com authoritative NS. enough here!
 6. local NS queries authoritative NS (ibm.com)
 7. ibm.com responds with the IP address of the server and stores the record in its cache valid for a given TTL.
 8. local NS gives IP address (recursive answer) to the client.

2c) After obtaining the DNS information, the local server stores it in its cache and the consumer is valid for a given TTL value.

2 a, The client wants to know the NS = "name server" of ibm.com domain



- If the name server is not cached in the proxy the proxy will ask the root server for which TLD (Top level domain) to ask for ".com" ?!
- Then when the TLD is known, the proxy will ask it for which Authoritative nameserver to talk to.
- The Authoritative nameserver replies the full name server, about itself!
- Proxy replies an answer to the query made by the user.

This is done with UDP

The queries and replies are done recursive and iterative when?

c) The local server will store the NS in its cache, so that it can be used next time to save time. TTL

- 3a) i) 2001:08b0:0002:0010:0000:0000:0000:0001
 ii) FF02:0000:0000:0000:0000:0001:ff6c:14dd
 iii) FE80:0000:0000:0000:020c:ffff:fe6c:14dd

- b) 2001:6b0:2:10::1 is type unicast
 FF02::1:ff6c:14dd is type multicast
 FE80::20c:ffff:fe6c:14dd is type unicast

unicast implies its an IPv6 address for a given interface.
 multicast implies it is an IPv6 address to a ~~group~~ a given group of interfaces. delivering packet to - -

- c) FF02::1:ff6c:14dd can not be used as a valid source address in IPv6 packet because it is a multicast address that handles a group of interfaces and hence can only be used as destination address.

- d) 2001:6b0:2:10::1 has scope global
 FF02::1:ff6c:14dd has scope ~~link~~ local
 FE80::20c:ffff:fe6c:14dd has scope link local

Scope global implies that it can be used to route packets over to the internet.
 link local implies that packets ~~are sent~~ can only be routed within a given network connected by physical interface.
 packets from these addresses can not be routed across a router

2+

2+

In the function `if (FD_ISSET (listenfd, &readfds)) {` ?

There are 2 system calls that cause blocking

- `accept()`

- `recv()`

A call to `accept` causes blocking in the function until either an error has occurred or a connection file descriptor, client has returned successfully.

This blocks all other clients trying to make connection with the server.

A call to ~~recv~~ `recv()` can also cause blocking.

less than 8 bits

If there is no data in the buffer to be read and the non-blocking flag is not set, the call will block until data is available to be read. ✓

If there is no data in the buffer to be read and the non-blocking flag is ~~not~~ set, the call will return an error and the `errno` will be set accordingly.

CHALMERS	Anonymous code EDA 3877	Points for question (to be filled in by teacher)	Consecutive page no. Löpande sid nr 6
	Anonymous kod	Poäng på uppgiften (att fyllas av läraren)	Question no. Uppgift nr 5
		2	
	<p>a) Adjustment: p_i does not ^① adjust its clock.</p> <p>Agreement: p_i's clock ^② agrees with the clock of ^③ all other correct processors that have also ^④ been working correctly for at least k time units.</p>		2p.
	<p>b) At the starting configuration, P will send every count and clock of its neighbours.</p> <p>Suppose P has been executing correctly and continues to execute correctly for successive steps of k; the set NB will not be empty because at least P will be in the set and continue to be in the set as long as it executes more than $k-2$ success steps correctly.</p> <p>This is because it is not behind any other processor.</p> <p>P continues to update all neighboury processors of the order given by $A_count[P] = B_count[P] + 1 \pmod{3}$.</p> <p>No, the question was why $R = NB \neq \emptyset$ after the first step.</p>		Op

6a) d is the diameter of the network.

② n is the number of nodes (hosts) in the network.

b) Converge-to-the-max depends on ' d ' and ' n ' whereas ~~converge-to-the-min~~ converge-to-the-min does not depend on ' n '.

In an actual system, as the number of nodes, n , increases at a much higher rate than the diameter hence converge-

to-the-max gets ~~higher~~ more values, M , for the bound which is not good.

Converge-to-the-mean is better for scalability because it is not affected by increase in node number of nodes, n .

c) Converge-to-the-max is easier to work with at application layer.

① This is because converge-to-the-min converges to a past clock value which can confuse the application.

d) 1. wraps around the zero value and ~~no~~ processor assigns zero to its ^{clock} value

2. d

3. induction proof

4. wraps around the zero value and assigns zero to its clock value.

5. d

6. $M-d$

7. d

8. case holds

③

(a) We prove this by way of contradiction. Suppose that in the starting configuration c , P_1 does not change the value of X_1 .

- Let c_1 be the configuration that immediately follows c in which P_2 is the only processor that can change the value of its variable X_2 , because c_1 leading to $X_1 = X_2$ and $X_2 \neq X_1$ for the remaining $n-2$ rounds.

- Let c_2 be the configuration that follows immediately after c_1 , such that only P_3 can change its register value $X_1 = X_2 = X_3$.

In general, the only processor that can change the value of its register for a given configuration c_i is P_i such that

~~$X_i = X_j$ for $2 \leq i < j < n$~~ $X_1 = X_i$ but $X_i \neq X_n$ for the next $n-i$ rounds

In the n th round, $X_n = X_1$ and therefore P_1 changes the value of X_1 once in n rounds. Contradiction. \square

7.5) Let c be the starting configuration for every fair execution.

from Lemma 2.2, ~~then~~ c is a safe configuration from which all X variables are equal.

Let c_1 be the configuration that follows c .

- Using Lemma 2.4, processor P_i changes ~~its~~ the values of its variable at least once in n rounds. hence
- Using Lemma 2.3 and knowing that in any round, only one processor can change the value of its register, the complexity of all $P_i (i \neq 1)$ changing their values is once in n rounds

~~Therefore in relation to~~
within

Therefore, after $O(n^2)$ rounds all the processors including P_1 have changed the value of their registers in relation to ME \square

<p>CHALMERS</p>	<p>Anonymous code EDA 387-7</p> <p>Anonymous kod</p>	<p>Points for question (to be filled in by student)</p> <p>Poäng på uppgiften (att fyllas av lärare)</p>	<p>Consecutive page no. Löpande sid nr 10</p> <p>Question no. Uppgift nr 8</p>
<p><u>Lemma:</u> Suppose that in an anonymous, synchronous uniform network of ring topology, there is no deterministic self-stabilising algorithm for token circulation, DTC. The processors take a step at the same time, each starting from the first line of the algorithm and controlled by a central daemon.</p>			
<p><u>Proof:</u></p> <p>We prove this by way of contradiction that suppose that a deterministic self-stabilising algorithm DTC for token ring circulation exists.</p> <p>Let the processors start in a configuration C_0 such that the starting execution is given by state S_i and register value r_i.</p> $E = \langle C_0, S_1, S_2, S_3, \dots, S_n, r_1, r_2, r_3, \dots, r_n \rangle$ <p>where by $S_i = S_j$ and register values $r_i = r_j$ for for successive configurations and $i \neq j$.</p> <p>The task of the DTC is to reach a configuration where all values of the registers are the same such that $r_i = r_j$ and $S_i = S_j$.</p> <p>Let $C_{\text{last-same}}$ and $C_{\text{first-different}}$ be the consecutive configurations for which $r_i = r_j$ and $S_i = S_j$ respectively hold and do not hold such that $C_{\text{first-different}} = C_{\text{last-same}} + 1$.</p> <p>It is clear that the DTC leads to a configuration set execution set in which two respective registers values hold different values.</p>			

3

CHALMERS	Anonymous code: EQA-387-7	Points for question (space filled in by teacher)	Consecutive page no. Löpande sid nr 41
	Anonym kod	Poäng på uppgiften (fylls av lärare)	Question no. Uppgift nr 8
<p>For a deterministic self-stabilizing algorithm for token ring circulation, we should have $s_i = s_j$ and $r_i = r_j$.</p> <p>Since the processors are identical and they take the same steps in execution.</p> <p>Therefore the DFC causes atleast one of the processors to execute a different step than all other processors and also have a different register value compared to all other processors which is a contradiction \square</p>			

a) A set of legal executions for a given algorithm is a given set of configurations run in fair execution and all belong exhibit a legal behavior.

Safe configuration for a given algorithm is one that is reached after a set of fair executions all of which belong to the set LE (Legal Execution).

b) No, the algorithm is not self-stabilizing
Starting configuration c when

$$\text{leader} := \text{id} := \text{minimum}\{\text{id} \mid c_m \in \mathcal{L}\}$$

a) The task of vertex color is to give colors to the nodes of a graph such that no two neighboring nodes P_i and P_j ($i \neq j$) have the same colour.

There is no upper bound on the number of colours that can be used.

The task can use $(\Delta + 1)$ colours where Δ is the upper bound on the number of colours.

b) The algorithm converges at the $O(n)$ where n is the number of nodes.

Consider a set of nodes connected to each other in form of a line as show below.



Suppose that the link ~~between~~ between P_{n-1} and P_n is not operative.

If the the link P_{n-1} to P_n recovers and assigns a color $color_n$ such that $color_n = color_{n-1}$, then every node in the

system, P_i , $1 \leq i \leq n$ will run the algorithm to change color hence $O(n)$.

